

FEB 28 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No..... 10/803,264  
Filing Date..... March 17, 2004  
Inventor..... Warren M. Farnworth et al.  
Assignee..... Micron Technology, Inc.  
Group Art Unit..... 2829  
Examiner..... Russell Marc Kobert  
Attorney Docket No..... MI22-2524  
Title: Method and Apparatus for Testing Semiconductor Circuitry for Operability  
and Method of Forming Apparatus for Testing Semiconductor Circuitry for  
Operability

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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5. Form PTO-1449

Dated: February 28, 2005

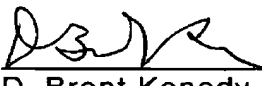
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D. Brent Kenady  
Telephone No. (509) 624-4276

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Total Number of Pages in This Submission 14

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First Named Inventor	Warren M. Farnworth
Art Unit	2829
Examiner Name	Russell Marc Kobert
Attorney Docket Number	MI22-2524

**ENCLOSURES** (Check all that apply)

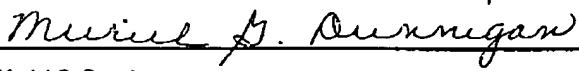
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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name	Wells St. John P.S.		
Signature			
Printed name	D. Brent Kenady		
Date	2-28-05	Reg. No.	40,045

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Signature			
Typed or printed name	Muriel G. Dunnigan	Date	February 28, 2005

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Response to November 29, 2004 OA

MI22-2524

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**RESPONSE TO NOVEMBER 29, 2004 OFFICE ACTION**

To: Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: D. Brent Kenady  
Tel. 509-624-4276; Fax 509-838-3424  
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601 W. First Avenue, Suite 1300  
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Responsive to the Office Action dated November 29, 2004, Applicant  
amends and remarks as follows:

**AMENDMENTS**

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.